

List of Publications

Frits Vaandrager

April 2, 2024

Google Scholar lists more than 11.5K citations to my publications and my Hirsch index is 57 (April 2024). For some venues, I also listed the acceptance ratio or classification according to <http://www.core.edu.au/>.

Books

1. D.K. Kaynar, N.A. Lynch, R. Segala and F.W. Vaandrager. The Theory of Timed I/O Automata, Second Edition. Synthesis Lecture on Distributed Computing Theory, Morgan & Claypool Publishers, 137 pages. 2010. (**Scholar: 310**)
2. J. Ouaknine and F.W. Vaandrager (editors). Proceedings of the 7th International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS 2009), Budapest, Hungary, September 14-16, 2009. Proceedings. LNCS 5813, Springer, X, 285 pages. 2009. ISBN 978-3-642-04367-3.
3. F.W. Vaandrager and J.H. van Schuppen, editors. Proceedings of the Second International Workshop on Hybrid Systems: Computation and Control (HSCC'99), Berg en Dal, The Netherlands, March 1999. LNCS 1569, Springer-Verlag, X, 271 pages. 1999.
4. G. Rozenberg and F.W. Vaandrager, editors. Lectures on Embedded Systems. LNCS 1494, Springer-Verlag, VIII, 423 pages. 1998.
5. Algebraic Techniques for Concurrency and Their Application. Ph.D. Thesis, University of Amsterdam, February 1990. CWI, Amsterdam, 244 pages. 1989.

Journal Papers

1. F.W. Vaandrager, M. Ebrahimi, and R. Bloem. Learning Mealy Machines with One Timer. In *Information and Computation*, 295, Part A, December 2023, 10513. Special Issue: Selected papers of the 15th International Conference on Language and Automata Theory and Applications, LATA 2021. <https://doi.org/10.1016/j.ic.2023.105013>.
2. F.W. Vaandrager and A. Midya. A Myhill-Nerode Theorem for Register Automata and Symbolic Trace Languages. In *Theoretical Computer Science* 912:37–55, 2022. Special issue *ICTAC'20*. <https://doi.org/10.1016/j.tcs.2022.01.015> (**CORE: A**)
3. P. van den Bos and F.W. Vaandrager. State identification for labeled transition systems with inputs and outputs. In *Science of Computer Programming*, Volume 209, 2021, 102678, ISSN 0167-6423, <https://doi.org/10.1016/j.scico.2021.102678>. (**CORE: A**)
4. M.G.T.A. Rutten, F.W. Vaandrager, J.A.A.W. Elemans, and R.J.M. Nolte. Encoding information into polymers. *Nature Reviews Chemistry* 2:365-381, 2018. DOI: 10.1038/s41570-018-0051-5. (**Scholar: 173**)

5. F.W. Vaandrager. Model Learning. *Communications of the ACM (CACM)* 60(2): 86-95. February 2017. Cover article. Reached top 3 of most downloaded CACM papers in February 2017 with 5K downloads in 6 weeks. 16284 downloads (April 28). Translated in Chinese and Spanish. **(Scholar: 243)**
6. F. Aarts, B. Jonsson, J. Uijen, and F.W. Vaandrager. Generating Models of Infinite-State Communication Protocols using Regular Inference with Abstraction. *Formal Methods in System Design* 46(1):1-41, 2015. **(CORE: A) (Scholar: 162)**
7. F. Verbeek and F.W. Vaandrager. Recreational Formal Methods: Designing Vacuum Cleaning Trajectories. *Bulletin of the EATCS* 113:100-109, June 2014.
8. F. Aarts, H. Kuppens, G.J. Tretmans, F.W. Vaandrager, and S. Verwer. Improving Active Mealy Machine Learning for Protocol Conformance Testing. *Machine Learning* 96(1-2): 189–224, 2014. **(CORE: A*) (Scholar: 70)**
9. F. Houben, G. Igna, and F.W. Vaandrager. Modeling Task Systems Using Parameterized Partial Orders. In *International Journal on Software Tools for Technology Transfer (STTT)*, 15(3):269-286, 2013. DOI: 10.1007/s10009-012-0264-8.
10. F. Heidarian, J. Schmaltz, and F.W. Vaandrager. Analysis of a Clock Synchronization Protocol for Wireless Sensor Networks. In *Theoretical Computer Science* 413: 87-105, 2012. Special issue on QAPL'10. DOI: 10.1016/j.tcs.2011.07.018. **(CORE: A) (Scholar: 81)**
11. J. Berendsen, B. Gebremichael, F.W. Vaandrager, and Miaomiao Zhang. Formal Specification and Analysis of Zeroconf using Uppaal. In *ACM Transactions on Embedded Computing Systems* 10(3), 2011. **(CORE: A) (Scholar: 60) (Using Uppaal we found 6 errors/ambiguities in the RFC for an internet protocol; our paper triggered further research on e.g. compositional verification and probabilistic model checking)**
12. J. Berendsen, D. Jansen J. Schmaltz, and F.W. Vaandrager. The Axiomatization of Override and Update. In *Journal of Applied Logic* 8:141-150, 2010. ISSN 1570-8683, DOI: 10.1016/j.jal.2009.11.001. **(CORE: A)**
13. R. Hamberg and F.W. Vaandrager. Using Model Checkers in an Introductory Course on Operating Systems. *ACM SIGOPS Operating Systems Review* 42(6):101-111, October 2008.
14. L. Cheung, M.I.A. Stoelinga, and F.W. Vaandrager. A Testing Scenario for Probabilistic Processes. In: *Journal of the ACM* 54(6), December 2007. **(CORE: A*) (Scholar: 104) (For this paper, Mariëlle Stoelinga was awarded the 2008 Professor De Winter prize for the best journal publication by a female scientist at the University of Twente.)**
15. N.A. Lynch, R. Segala, and F.W. Vaandrager. Observing Branching Structure Through Probabilistic Contexts. In: *SIAM Journal on Computing* 37(4):977-1013, 2007. **(CORE: A*) (Scholar: 71)**
16. F.W. Vaandrager and A.L. de Groot. Analysis of a Biphase Mark Protocol with Uppaal and PVS. In *Formal Aspects of Computing Journal* 18(4):433-458, December 2006. **(CORE: A) (Scholar: 62)**
17. L. Cheung, N.A. Lynch, R. Segala, and F.W. Vaandrager. Switched PIOA: Parallel Composition via Distributed Scheduling. In *Theoretical Computer Science* 365(1-2):83-108, 2006. **(CORE: A) (Scholar: 67)**
18. M. Hendriks, N.J.M. van den Nieuwelaar, and F.W. Vaandrager. Model Checker Aided Design of a Controller for a Wafer Scanner. In *International Journal on Software Tools for Technology Transfer (STTT)* 8(6):633–647, Special Section on Quantitative Analysis of Real-time Embedded Systems, 2006.

19. W.O.D. Griffioen and F.W. Vaandrager. A Theory of Normed Simulations. In *ACM Transactions on Computational Logic (TOCL)* 5(4):577–610, October 2004. **(CORE: A)**
20. N.A. Lynch, R. Segala, and F.W. Vaandrager. Hybrid I/O automata. *Information and Computation* 185(1):105–157, August 2003. **(Scholar: 585)**
21. T.S. Hune, J.M.T. Romijn, M.I.A. Stoelinga, and F.W. Vaandrager. Linear parametric model checking of timed automata. *The Journal of Logic and Algebraic Programming* 52-53:183–220, 2002. **(Scholar: 197)**
22. J.G. Springintveld, F.W. Vaandrager, and P.R. D’Argenio. Testing Timed Automata. *Theoretical Computer Science* 254(1–2):225–257, March 2001. **(CORE: A) (Scholar: 388)**
23. M.C.A. Devillers, W.O.D. Griffioen, J.M.T. Romijn, and F.W. Vaandrager. Verification of a Leader Election Protocol: Formal Methods Applied to IEEE 1394. *Formal Methods in System Design*, 16(3):307–320, June 2000. **(CORE: A) (Scholar: 105)**
24. R.J. van Glabbeek and F.W. Vaandrager. The difference between splitting in n and $n + 1$. *Information and Computation*, 136(2):109–142, August 1997. **(Scholar: 61)**
25. J.M.T. Romijn and F.W. Vaandrager. A note on fairness in I/O automata. *Information Processing Letters*, 59(5):245–250, 1996.
26. N.A. Lynch and F.W. Vaandrager. Action transducers and timed automata. *Formal Aspects of Computing*, 8(5):499–538, Sept–Oct 1996. **(Scholar: 105)**
27. N.A. Lynch and F.W. Vaandrager. Forward and backward simulations, II: Timing-based systems. *Information and Computation*, 128(1):1–25, July 1996. **(Scholar: 198)**
28. N.A. Lynch and F.W. Vaandrager. Forward and backward simulations, I: Untimed systems. *Information and Computation*, 121(2):214–233, September 1995. **(Scholar: 668)**
29. R. De Nicola and F.W. Vaandrager. Three logics for branching bisimulation. *Journal of the ACM*, 42(2):458–487, March 1995. **(Scholar: 499)**
30. L. Aceto, B. Bloom, and F.W. Vaandrager. Turning SOS rules into equations. *LICS’92 Special Issue of Information and Computation*, 111(1):1–52, May 1994. **(Scholar: 196)**
31. R.J. van Glabbeek and F.W. Vaandrager. Modular specification of process algebras. *Theoretical Computer Science*, 113(2):293–348, 1993. **(CORE: A) (Scholar: 60)**
32. J.F. Groote and F.W. Vaandrager. Structured operational semantics and bisimulation as a congruence. *Information and Computation*, 100(2):202–260, October 1992. **(Scholar: 448)**
33. J.C.M. Baeten and F.W. Vaandrager. An algebra for process creation. *Acta Informatica*, 29(4):303–334, 1992.
34. F.W. Vaandrager. Determinism \rightarrow (event structure isomorphism = step sequence equivalence). *Theoretical Computer Science*, 79:275–294, 1991. **(CORE: A)**

Conference Papers and Refereed Articles in Books

1. V. Bruyère, B. Garhewal, G.A. Pérez, G. Staquet, and F.W. Vaandrager. Active Learning of Mealy Machines with Timers. Submitted. Full version available as CoRR arxiv:2403.02019, March 2024.

2. V. Bruyère, G.A. Pérez, G. Staquet and F.W. Vaandrager. Automata with Timers. In L. Petrucci and J. Sproston, editors. *Proceedings Formal Modeling and Analysis of Timed Systems - 21st International Conference, FORMATS 2023*, Antwerp, Belgium, September 19-21, 2023. Lecture Notes in Computer Science 14138, pages 33–49, Springer 2023, ISBN 978-3-031-42625-4. **This paper received the Oded Maler Award in Timed Systems for best paper.**
Full version available as CoRR arxiv:2305.07451, May 2023.
3. L. Kruger, B. Garhewal and F.W. Vaandrager. Lower Bounds for Active Automata Learning. In *Proceedings 16th International Conference on Grammatical Inference (ICGI 2023)*, July 10-13, 2023, Rabat, Morocco.
4. F.W. Vaandrager and T. Wißmann. Action Codes. In K. Etessami, U. Feige, and G. Puppis, editors, *50th International Colloquium on Automata, Languages, and Programming (ICALP 2023)*, volume 261 of *Leibniz International Proceedings in Informatics (LIPIcs)*, pages 137:1–137:20, Dagstuhl, Germany, 2023. Schloss Dagstuhl – Leibniz-Zentrum für Informatik. **(Acceptance rate 30%)**.
Full version available as CoRR arxiv:2301.00199, January 2023.
5. F.W. Vaandrager, B. Garhewal, J. Rot, and T. Wißmann. A New Approach for Active Automata Learning Based on Apartness. In: Fisman, D., Rosu, G. (eds) *Tools and Algorithms for the Construction and Analysis of Systems (TACAS 2022)*. Lecture Notes in Computer Science, vol 13243. Springer, Cham., 2022. **(Acceptance rate research papers 29%) (Scholar: 41)**
6. F.W. Vaandrager, M. Ebrahimi, and R. Bloem. Learning Mealy Machines with One Timer. In *Proceedings 14th-15th International Conference on Language and Automata Theory and Applications (LATA 2020/2021)*, Milan, Italy, March 1-5, 2021.
Full version appeared in *Information and Computation*.
7. F.W. Vaandrager and A. Midya. A Myhill-Nerode Theorem for Register Automata and Symbolic Trace Languages. In *Proceedings ICTAC'20*.
Full version appeared in *Theoretical Computer Science*.
8. B. Garhewal, F.W. Vaandrager, F. Howar, T. Schrijvers, T. Lenaerts, and R. Smit. Grey-Box Learning of Register Automata. In *Proceedings iFM'20*.
Full version available as CoRR arXiv:2009.09975, September 2020.
9. P. van den Bos and F.W. Vaandrager. State identification for labeled transition systems with inputs and outputs. In Farhad Arbab and Sung-Shik Jongmans, editors, *Formal Aspects of Component Software - 16th International Conference, FACS 2019, Amsterdam, The Netherlands, October 23-25, 2019, Proceedings*, volume 12018 of *Lecture Notes in Computer Science*, pages 191–212. Springer, 2019. **(Best paper award)**
Full version appeared in *Science of Computer Programming*.
10. R. Janssen, F.W. Vaandrager, and G.J. Tretmans. Relating Alternating Relations for Conformance and Refinement. In Wolfgang Ahrendt, Silvia Lizeth Tapia Tarifa. *Proceedings Integrated Formal Methods - 15th International Conference, IFM 2019, Bergen, Norway, December 2-6, 2019, Lecture Notes in Computer Science 11918*, pages 246–264, Springer 2019. **(Acceptance rate 29%)**
Full version available as CoRR arXiv:1909.13604, September 2019.
11. B. Jonsson, F. Howar, and F.W. Vaandrager. Combining Black-Box and White-Box Techniques for Learning Register Automata. In B. Steffen and G. Woeginger, editors. *Computing and Software Science, LNCS 10000*, pages 563-588, Springer, Cham, 2019.

12. F.W. Vaandrager. Automata learning and Galois connections (invited talk). In C. Baier, I. Chatzigiannakis, P. Flocchini, and S. Leonardi, editors, 46th International Colloquium on Automata, Languages, and Programming, ICALP 2019, July 9-12, 2019, Patras, Greece., volume 132 of LIPIcs, pages 4:1-4:1. Schloss Dagstuhl - Leibniz-Zentrum fuer Informatik, 2019.
13. D. Neider, R. Smetsers, F.W. Vaandrager, and H. Kuppens. Benchmarks for Automata Learning and Conformance Testing. In T. Margaria, K.G. Larsen and S. Graf, editors. Models, Mindsets, Meta: The What, the How, and the Why Not? LNCS 11200, Springer Nature Switzerland, 2019.
14. M. Jasper, M. Mues, A. Murtovi, M. Schlüter, F. Howar, B. Steffen, M. Schordan, D. Hendriks, R.R.H. Schiffelers, H. Kuppens, F.W. Vaandrager. RERS 2019: Combining Synthesis with Real-World Models. In D. Beyer, M. Huisman, F. Kordon, and B. Steffen, editors. International Conference on Tools and Algorithms for the Construction and Analysis of Systems - 25 Years of TACAS: TOOLympics, Held as Part of ETAPS 2019, Prague, Czech Republic, April 6-11, 2019, Proceedings, Part III. LNCS 11429, pp 101-115, Springer 2019.
15. A. Linard, C. de la Higuera, and F.W. Vaandrager. Learning Unions of k -Testable Languages. In Carlos Martín-Vide, Alexander Okhotin, Dana Shapira, editors. Language and Automata Theory and Applications - 13th International Conference, LATA 2019, St. Petersburg, Russia, March 26-29, 2019, Proceedings. Lecture Notes in Computer Science 11417, Springer 2019, ISBN 978-3-030-13434-1 Full version available as CoRR arXiv:1812.08269, December 2018. **(Acceptance rate 32%)**
16. R. Smetsers, P. Fiterau-Broştean, and F.W. Vaandrager. Model Learning as a Satisfiability Modulo Theories Problem. In S.T. Klein et al, editors. Proceedings 12th International Conference on Language and Automata Theory and Applications (LATA 2018), Bar-Ilan, Israel, April 9-11, 2018. LNCS 10792, pp. 182-194, 2018.
17. P. Fiterău-Broştean, T. Lenaerts, E. Poll, J. de Ruiters, F.W. Vaandrager, and P. Verleg. Model Learning and Model Checking of SSH Implementations. In H. Erdogmus and K. Havelund, editors. *Proceedings 24th ACM SIGSOFT International SPIN Symposium on Model Checking of Software*, 13-14 July 2017, Santa Barbara, CA, USA, pages 142-151. ACM. Dataset for this paper deposited at <https://doi.org/10.17026/dans-z6n-dxq6>. **(Scholar: 100)**
18. P. Fiterău-Broştean, R. Janssen, and F.W. Vaandrager. Combining Model Learning and Model Checking to Analyze TCP Implementations. In S. Chaudhuri and A. Farzan, editors. *Proceedings 28th International Conference on Computer Aided Verification (CAV'16)*, Toronto, Ontario, Canada, July 17-23, 2016. LNCS 9780, pp. 454-471, Springer, 2016. **(CORE: A*) (Acceptance rate 28%) (Scholar: 160)**
19. M. Schuts. J. Hooman and F.W. Vaandrager. Refactoring of Legacy Software using Model Learning and Equivalence Checking: an Industrial Experience Report. In E. Abraham and M. Huisman, editors. *Proceedings 12th International Conference on integrated Formal Methods (iFM)*, Reykjavik, Iceland, June 1-3, 2016. LNCS 9681, pp. 311-325, Springer, 2016. **(Acceptance rate 30%) (Scholar: 59)**
20. P. van den Bos, R. Smetsers, and F.W. Vaandrager. Enhancing Automata Learning by Log-Based Metrics. In E. Abraham and M. Huisman, editors. *Proceedings 12th International Conference on integrated Formal Methods (iFM)*, Reykjavik, Iceland, June 1-3, 2016. LNCS 9681, pp. 295-310, Springer, 2016. **(Acceptance rate 30%)**
21. W. Smeenk, J. Moerman, F.W. Vaandrager, and D.N. Jansen. Applying Automata Learning to Embedded Control Software. In M. Butler, S. Conchon and F. Zaidi, editors. *Proceedings 17th International Conference on Formal Engineering Methods (ICFEM 2015)*, Paris, 3-6 November 2015. LNCS, Springer, 2015. **(Acceptance rate 32%) (Scholar: 82)**

22. F. Aarts, P. Fiterău-Broștean, H. Kuppens, and F.W. Vaandrager. Learning Register Automata with Fresh Value Generation. In M. Leucker, C. Rueda and F.D. Valencia, editors. *Proceedings 12th International Colloquium on Theoretical Aspects of Computing (ICTAC 2015)*, Cali, Colombia, October 29-31. LNCS 9399, pp. 165-183, Springer Verlag, 2015. **(Acceptance rate 35%) (Scholar: 58)**
23. F. Aarts, H. Kuppens, F. Howar, and F.W. Vaandrager. Algorithms for Inferring Register Automata - A Comparison of Existing Approaches. In T. Margaria and B. Steffen, editors. *Proceedings 6th International Symposium On Leveraging Applications of Formal Methods, Verification and Validation (ISOLA 2014)*, 8-11 October 2014, Imperial, Corfu, Greece. LNCS 8802, pp. 202-219, Springer Verlag, 2014.
24. R. Smetsers, M. Volpato, F.W. Vaandrager, and S. Verwer. Bigger is Not Always Better: On the Quality of Hypothesis in Active Automata Learning. In A. Clark, M. Kanazawa, and R. Yoshinaka. *Proceedings 12th International Conference on Grammatical Inference (ICGI 2014)*, Kyoto, Japan, September 17-19, 2014, JMLR 34: 167-181.
25. P. Fiterău-Broștean, R. Janssen, and F.W. Vaandrager. Learning Fragments of the TCP Network Protocol. In F. Lang and F. Flammini, editors. *Proceedings 19th International Workshop on Formal Methods for Industrial Critical Systems (FMICS'14)*, Florence, Italy, September 11-12, 2014. LNCS 8718, pp. 78-93, Springer-Verlag, 2014.
26. A. Jalil Boudjadar, F.W. Vaandrager, J.-P. Bodeveix, and M. Filali. Extending UPPAAL for the Modeling and Verification of Dynamic Real-Time Systems. In F. Arbab and M. Sirjani, editors. *Proceedings Fundamentals of Software Engineering (FSEN'13)*, 24-26 April 2013, Tehran, Iran. LNCS 8161, pp. 111-132, Springer-Verlag, 2013.
27. M. Hendriks and F.W. Vaandrager. Reconstructing Critical Paths from Execution Traces. In *Proceedings 10th IEEE/IFIP International Conference on Embedded and Ubiquitous Computing (EUC'12)*, 5-7 December 2012, Paphos, Cyprus. IEEE Computer Society, pages 524-531, 2012. DOI 10.1109/ICCSE.2012.78.
28. A. Boudjadar, F.W. Vaandrager, J.-P. Bodeveix, and M. Filali. Callable Timed Automata: Adding Process Creation and Process Calls to UPPAAL. In *Proceedings 12th International Workshop on Automated Verification of Critical Systems (AVoCS)*, 18-20th September 2012, Bamberg, Germany.
29. F.W. Vaandrager. Active learning of extended finite state machines. In B. Nielsen and C. Weise, editors, *Proceedings 24th IFIP WG 6.1 International Conference on Testing Software and Systems (ICTSS 2012)*, Aalborg, Denmark, November 19-21, 2012, LNCS 7641, pages 5-7. Springer Berlin Heidelberg, 2012.
30. F. Aarts, H. Kuppens, G.J. Tretmans, F.W. Vaandrager, and S. Verwer. Learning and Testing the Bounded Retransmission Protocol. In J. Heinz, C. de la Higuera, and T. Oates, editors. *Proceedings 11th International Conference on Grammatical Inference (ICGI 2012)*, September 5 - 8, 2012, University of Maryland, College Park, USA. JMLR Workshop and Conference Proceedings 21:4-18, 2012.
Full version appeared in *Machine Learning*.
31. F. Aarts, F. Heidarian, and F.W. Vaandrager. A Theory of Abstractions for Learning I/O Automata. In M. Koutny and I. Ulidowski, editors. *Proceedings 23rd International Conference on Concurrency Theory (CONCUR)*, Newcastle upon Tyne, UK, September 3-8, 2012. LNCS 7454, pp. 240-255, Springer-Verlag, 2012. **(CORE: A) (Acceptance rate 36%)**
32. F. Aarts, F. Heidarian, H. Kuppens, P. Olsen, and F.W. Vaandrager. Automata Learning Through Counterexample-Guided Abstraction Refinement. In D. Giannakopoulou and D.

- Méry, editors. *Proceedings 18th International Symposium on Formal Methods (FM 2012)*, Paris, France, August 27-31, 2012. Springer-Verlag 2012. LNCS 7436, pp. 10-27, Springer-Verlag, 2012. **(CORE: A) (Acceptance rate 26%) (Scholar: 95)**
33. F. Houben, G. Igna, and F.W. Vaandrager. Modeling Task Systems Using Parameterized Partial Orders. In M. Di Natale, editor. *Proceedings 18th IEEE Real-Time and Embedded Technology and Applications Symposium (RTAS 2012)*, Beijing China, April 16-19, 2012. IEEE Computer Society, pp 317-327, 2012. **(CORE: A) (Acceptance rate 24%)**
 34. F. Aarts, J. Schmaltz, and F.W. Vaandrager. Inference and Abstraction of the Biometric Passport. In T. Margaria and B. Steffen, editors. *Proceedings 4th International Symposium On Leveraging Applications of Formal Methods, Verification and Validation (ISoLA 2010)*, 18-20 October 2010 - Amirandes, Heraclion, Crete. LNCS 6415, pp. 673-686, Springer-Verlag, 2010. **(Scholar: 113)**
 35. T. Basten, E. van Benthum, M. Geilen, M. Hendriks, F. Houben, G. Igna, F. Reckers, S. de Smet, L. Somers, E. Teeselink, N. Trcka, F. Vaandrager, J. Verriet, M. Voorhoeve, and Y. Yang. Model-Driven Design-Space Exploration for Embedded Systems: The Octopus Toolset. In T. Margaria and B. Steffen, editors. *Proceedings 4th International Symposium On Leveraging Applications of Formal Methods, Verification and Validation (ISoLA 2010)*, 18-20 October 2010 - Amirandes, Heraclion, Crete. LNCS 6415, pp. 90-105, Springer-Verlag, 2010. **(Scholar: 83)**
 36. G. Igna and F.W. Vaandrager. Verification of Printer Datapaths using Timed Automata. In T. Margaria and B. Steffen, editors. *Proceedings 4th International Symposium On Leveraging Applications of Formal Methods, Verification and Validation (ISoLA 2010)*, 18-20 October 2010 - Amirandes, Heraclion, Crete. LNCS 6416, pp. 412-423, Springer-Verlag, 2010.
 37. J. Berendsen, D.N. Jansen, and F.W. Vaandrager. Fortuna: Model Checking Priced Probabilistic Timed Automata. In *Proceedings QEST'2010*, Williamsburg, Virginia, USA, September 15 - 18, 2010. **(Acceptance rate 48%)**
 38. F. Aarts and F.W. Vaandrager. Learning I/O Automata. In P. Gastin and F. Laroussinie, editors. *Proceedings CONCUR'10*, Paris, France, August 31st - September 3rd, 2010. LNCS 6269, pp. 71-85, Springer-Verlag, 2010. **(CORE: A) (Acceptance rate 33%) (Scholar: 141)**
 39. M. Schuts, F. Zhu, F. Heidarian, and F.W. Vaandrager. Modelling Clock Synchronization in the Chess gMAC WSN Protocol. In S. Andova et.al., editors. *Proceedings First Workshop on Quantitative Formal Methods: Theory and Applications (QFM'09)*. Eindhoven, The Netherlands, 3rd November 2009. Electronic Proceedings in Theoretical Computer Science 13, pp. 41-54, 2009.
 40. I. AlAttili, F. Houben, G. Igna, S. Michels, F. Zhu, and F.W. Vaandrager. Adaptive Scheduling of Data Paths using Uppaal Tiga. In S. Andova et.al., editors. *Proceedings First Workshop on Quantitative Formal Methods: Theory and Applications (QFM'09)*. Eindhoven, The Netherlands, 3rd November 2009. Electronic Proceedings in Theoretical Computer Science 13, pp. 1-12, 2009.
 41. F. Heidarian, J. Schmaltz, and F.W. Vaandrager. Analysis of a Clock Synchronization Protocol for Wireless Sensor Networks. In A. Cavalcanti and D. Dams, editors. *Proceedings 16th International Symposium of Formal Methods (FM2009)*, Eindhoven, the Netherlands, November 2-6, 2009. LNCS 5850, pp. 516-531, Springer-Verlag. **(Acceptance rate 26%) (CORE: A)**
Full version appeared in *Theoretical Computer Science*.

42. F.W. Vaandrager, D.N. Jansen, and E. Koopmans. Een Module over Model Checking voor het VWO. In F. Vodegel en M. Loots, editors. *Proceedings NIOC 2009*, Utrecht, 7-8 April 2009, pp. 135–137. Drukkerij Damen, Werkendam.
43. J. Berendsen and F.W. Vaandrager. Compositional Abstraction in Real-Time Model Checking. In F. Cassez and C. Jard, editors. *Proceedings Sixth International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS 2008)*, September 15-17, 2008, Saint-Malo, France. 2008. LNCS 5215, pp. 233-249, Springer-Verlag.
44. G. Igna, V. Kannan, Y. Yang, T. Basten, M. Geilen, F.W. Vaandrager, M. Voorhoeve, S. De Smet, and L. Somers. Formal Modeling and Scheduling of Data Paths of Digital Document Printers. In F. Cassez and C. Jard, editors. *Proceedings Sixth International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS 2008)*, September 15-17, 2008, Saint-Malo, France. 2008. LNCS 5215, pp. 169-186, Springer-Verlag.
45. B. Gebremichael, F.W. Vaandrager, and M. Zhang. Analysis of a Protocol for Dynamic Configuration of IPv4 Link Local Addresses using Uppaal. In *Proceedings of the 6th Annual ACM & IEEE Conference on Embedded Software (EMSOFT 2006)*, Seoul, South Korea, October 22-25, 2006. ACM Press, pages 242-251. **(CORE: A) (Acceptance rate 33%)** Full version appeared in *ACM TECS*.
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